

APPLICATION NO. ATTY DOCKET NO. Form PTO-1449 2834-0133P Div.of 09/381,994 APPLICANT INFORMATION DISCLOSURE CITATION GUDESEN et al. IN AN APPLICATION FILING DATE GROUP (Use several sheets if necessary) 2813 October 17, 2001 U.S. PATENT DOCUMENTS CLASS FILING DATE EXAMINER DOCUMENT NUMBER DATE NAME 1991-08-27 Sonneschein et al. 1995-01-03 Mantell 5 3 7 8 1 6 9 1997-10-14 Smayling 5 6 7 7 0 4 1 Nathan et al 5 5 3 7 0 8 1996-07-16 1 5 8 0 8 3 5 1 1998-09-15 Nathan et al 5 6 8 3 4 1 1997-11-11 Roesner 6 5 5 7 2 4 0 9 1996-11-05 Nathan et al Potember et al 4 3 1983-02-01 3 7 8 8 4 8 0 6 9 5 1989-02-21 Day et al 8 1989-04-25 Potember et al 8 2 5 0 FOREIGN PATENT DOCUMENTS TRANSLATION CLASS DOCUMENT NUMBER DATE COUNTRY YES NO 95 1995-04-06 **WPO** 0 3 8 3 1989-12-06 **EPO** 0 4 3 6 OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.) A.R. Brown et al., "LOGIC GATES MADE FROM PLOYMER TRANSISTORS AND THEIR USE IN RING OSCILLATORS", SCIENCE, Vol. 270, 11/10/95. Francis Garnier et al., "All-Polymer Field-Effect Transistor Realized by Printing Techniques", SCIENCE, Vol. 265, 9/16/94 D.M. deLeeuw et al., "Polymeric Integrated Circuits and Light-Emitting Diodes", IEEE, (1997) ZY Hua et al., "A New Material for Optical, Electrical and Electronic Thin Film Memories", Vacuum, Vol. 43, 11:1019-1023, (1992) Yen-Yi LIN et al., "Pentacene-Based Organic Thin-film Transistors", IEEE Transactions on Electron Devices, Vol. 44, No. 8, 8/97

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